AMENDMENT UNDER 37 C.F.R. § 1.116 U.S. APPLN. NO. 09/650,726 ATTORNEY DOCKET NO. Q60462

## AMENDMENTS TO THE SPECIFICATION

## Paragraph bridging pages 2 and 3:

To this end, the invention provides a method which <u>comprises eonsists in-processing</u> some of the output signals from said unit as they are issued by means of at least one programmable logic circuit, in storing values of parameters corresponding to said processed signals, and in causing said microprocessor to access said stored parameter values at a frequency which is compatible with its own operating frequency.

## Paragraph bridging pages 3 and 4:

• The method <u>comprises eonsists in sending</u> at least some of the signals generated by the microprocessor to at least a second programmable logic circuit and in sending to the electronic unit simulation signals generated by said second programmable logic circuit while the microprocessor is not in communication with the unit. In other words, the orders generated by the microprocessor are delivered to the electronic unit via the second programmable logic circuit during a period in which the microprocessor is busy, e.g. calculating the next control parameters. By means of this aspect of the invention, it is possible to control the unit with signals that are issued with very great precision, of microsecond order, which is representative of certain sensors, such as an incremental speed sensor which detects the positions of notches on a disk with precision that is considerably shorter than several tens of microseconds.

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